

Declaration for Patent Application

Docket Number: 0811.1220000

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter that is claimed and for which a patent is sought on the invention entitled: **Advanced Schematic Editor**,

the specification of which is attached hereto unless the following box is checked:

- ☐ was filed on _____;
as United States Application Number or PCT International Application Number _____; and
was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information that is material to patentability as defined in 37 C.F.R. § 1.56, including for continuation-in-part applications, material information that became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or (f), or § 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or § 365(a) of any PCT international application, which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent, inventor's or plant breeder's rights certificate(s), or PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Claimed

2,315,548
(Application No.)

Canada
(Country)

09/08/2000
(Day/Month/Year Filed)

☒ Yes ☐ No

(Application No.)

(Country)

(Day/Month/Year Filed)

☐ Yes ☐ No

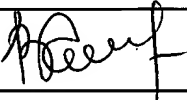
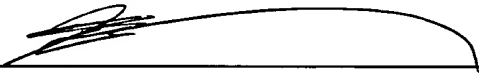
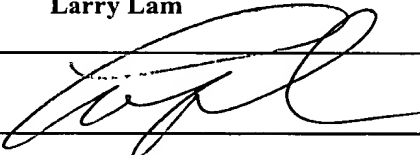
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor	Val Gont	
Signature of sole or first inventor		Date July 12, 2001
Residence	62 Longden Place, Kanata, Ontario, K2M 1T9 V.G. July 12, 2001 K2M 1G9, Canada	
Citizenship	Canadian	
Mailing Address	Same as Above	
Full name of second inventor	Jason Abt	
Signature of second inventor		Date July 16, 2001
Residence	199 Old Colony Road, Kanata, Ontario, K2L 1M6, Canada	
Citizenship	Canadian	
Mailing Address	Same as Above	
Full name of third inventor	Larry Lam	
Signature of third inventor		Date July 12/2001
Residence	646 Arc-En-Ciel Street, Orleans, Ontario, K4A 3H8, Canada	
Citizenship	Canadian	
Mailing Address	Same as Above	

POWER OF ATTORNEY FROM ASSIGNEE

Semiconductor Insights Inc., a corporation of Ontario, Canada, having a principal place of business at 3000 Solandt Road, Ottawa, Ontario, K2K 2X2, Canada, is assignee of the entire right, title and interest for the United States of America (as defined in 35 U.S.C. § 100), by reason of an Assignment to the Assignee of an invention known as Advanced Schematic Editor, which is disclosed and claimed in a patent application of the same title by the inventor(s) Val Gont, Jason Abt, and Larry Lam (said application filed on even date at the U.S. Patent and Trademark Office).

The Assignee hereby appoint the U.S. attorneys associated with **CUSTOMER NUMBER 28393** to prosecute this application and any continuation, divisional, continuation-in-part, or reissue application thereof, and to transact all business in the U.S. Patent and Trademark Office connected therewith, such attorneys currently being: Robert Greene Sterne, Esq., Reg. No. 28,912; Edward J. Kessler, Esq., Reg. No. 25,688; Jorge A. Goldstein, Esq., Reg. No. 29,021; David K.S. Cornwell, Esq., Reg. No. 31,944; Robert W. Esmond, Esq., Reg. No. 32,893; Tracy-Gene G. Durkin, Esq., Reg. No. 32,831; Michele A. Cimbala, Esq., Reg. No. 33,851; Michael B. Ray, Esq., Reg. No. 33,997; Robert E. Sokohl, Esq., Reg. No. 36,013; Eric K. Steffe, Esq., Reg. No. 36,688; Michael Q. Lee, Esq., Reg. No. 35,239; Steven R. Ludwig, Esq., Reg. No. 36,203; John M. Covert, Esq., Reg. No. 38,759; and Linda E. Alcorn, Esq., Reg. No. 39,588. The Assignee hereby grants said attorneys the power to insert on this Power of Attorney any further identification that may be necessary or desirable in order to comply with the rules of the U.S. Patent and Trademark Office.

Send correspondence to:

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FOR: Semiconductor Insights Inc.
SIGNATURE: [Signature]
BY: DOLG SMEATON
TITLE: PRES. & C.E.O.
DATE: JULY 12/01